

FORM PTO-1449
(Rev. 7-80)

U.S. Department of Commerce
Patent and Trademark Office

Attorney Docket No.
TESSERA 3.0-078 DIV

Serial No.

09/02/97

Applicant
Joseph Fjelstad

Filing Date 2-9-98
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2800

LIST OF PRIOR ART CITED BY APPLICANT

(Use several sheets if necessary)

U.S. PATENT DOCUMENTS

*EXAMINER INITIALS		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
A	AA	5,148,265	07/92	Khandros et al.			
A	AB	5,148,266	07/92	Khandros et al.			
A	AC	5,455,390	10/95	DiStefano et al.			
A	AD	4,618,878	10/96	Aoyama et al.			
A	AE	4,365,264	12/82	Mukai et al.			
A	AF	5,604,380	07/97	Nishimirra et al.			
A	AG	5,430,329	07/95	Harada et al.			
A	AH	5,070,297*	12/3/91	Kwon et al.			
A	AI	4,001,870	01/77	Saiki et al.			
	AJ						
	AK						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
							YES NO
	AL						
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OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

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Applicant
Joseph Fjelstad

Filing Date
February 9, 1998

Group
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U.S. PATENT DOCUMENTS

*EXAMINER INITIALS		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
Q	AA	4,813,129	03/21/89	Karnazos			
Q	AB	4,885,126	12/05/89	Polonio			
Q	AC	4,902,606	02/20/90	Patraw			
Q	AD	5,072,520	12/17/91	Nelson			
Q	AE	5,180,311	01/19/93	Schreiber et al.			
Q	AF	5,666,270	09/09/97	Matsuda et al.			
Q	AG	5,679,977	10/21/97	Khandros et al.			
Q	AH	5,749,997	05/12/98	Tang et al.			
	AI						
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FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
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	AN							
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OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

Q	AR		"Methods of Testing Chips and Joining Chips to Substrates," 2244 Research Disclosure, February 1991, Elmsworth, GB, 32290					
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U.S. PATENT DOCUMENTS

*EXAMINER INITIALS		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
<i>Q</i>	AA	5,777,379	7/7/98	Karavakis et al.	257	668	
	AB						
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